

# ST2 0825Nx\SR ST2 0825Nx\DR 



## DATA SHEET

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## Attention

The information contained in this document may change without notice.
Therefore, please check our website (www.kernelgroup.it) regularly and always download the latest version available.

## 1 SAFETY

This product is a String Monitoring System (SMU) and its application is the monitoring of photovoltaic systems. From an electrical point of view, by their nature, photovoltaic systems operate in direct current and with very high voltage and current values.

## Safety : General Conditions

| DANGER | - This product is therefore designed to operate with direct current voltage <br> values up to 1500 VDC. This voltage value is potentially fatal. Voltage values <br> between 24 VDC and 1500 VDC are therefore present on this product. |
| :--- | :--- | :--- |
| - All transducers connected to the auxiliary input connectors must be isolated at |  |
| $1500 \mathrm{VDC}$. |  |
| These transducers are PT100 and, in the case of the ST2N and ST2 series, also |  |
| transducers with $0 / 10 \mathrm{VDC}$ and $0 / 20 \mathrm{~mA}$ output (typically anemometers) |  |

Safety : Installation and replacement precautions

|  | - The installation and / or replacement of this product must take place in <br> absolute safety, therefore it is necessary to proceed with the installation and $/$ <br> or replacement of this product which is housed inside the field panel after <br> disconnecting the power and dangerous voltages from solar panels. |
| :--- | :--- |
| DANGER | - In any case, any intervention involving the handling of the SMU must be carried <br> out by skilled and trained technicians equipped with insulation gloves designed <br> for voltage values up to 1500 VDC and after disconnecting any voltage and <br> current from the solar panels. |

## Important Safety Instructions

|  | DANGER | - Contact with wiring terminals inside the device can cause death by electric shock! <br> - Before to operate inside cards with tester, or with other measurement equipment, it is mandatory TO OPEN fuses and other components that can bring high level voltage inside cards. |
| :---: | :---: | :---: |
|  | WARNING | - Please check all input and output wire terminals in case of high DC voltage and make sure there is no voltage before electrical connection to avoid electric shock! <br> - Do not touch the live parts of the input and output sides to avoid electric shock when checking or maintaining the device. <br> - All installation and wiring connections must be made by qualified technical personnel only. |
|  | CAUTION | - All wiring and operation must comply with the requirements of the relevant local standards of the device. <br> - Check the device and make sure there are no problems with the installation before putting it into operation! <br> - Connect the wires to the positive and negative marked positions of the device to avoid the risk of short circuit, ensure personal safety and keep the device in normal operation. |
| 0 | IMPORTANT | Reference to current standards <br> For any other consideration, safety precaution, it is absolutely necessary, before carrying out any installation and / or replacement of the SMU, to refer to the regulations in force regarding the construction of combiner boxes. |

## 2 GENERAL NOTES

### 2.1 Introduction

The ST2 module to string control, allow to monitoring current and voltage generated by photovoltaic panels strings. For example is possible connect each other 32 PV panels at 36 V to each string channel, with positive pole connected each other. The negative pole of each string is brought to the dedicated input, like in the following picture :


After the strings input connector, on the ST2 board there are two resistors in parallel, they are necessary to detect the current follow :

and finally a copper bar connect all the negative poles, thus creating a common 0 V .
The ST2 board also provides four digital inputs and three analog inputs. One analog inputs is for PT100 connection, another one for solarimeter connection and the last analog input is $0 \ldots 20 \mathrm{~mA}$ with precision better than $1,5 \%$. The digital inputs allows to detect the arrester state, another input is usually used for a sensor connected to string box panel's closing door and other switches in general, and an auxiliary fourth PNP digital input.
Is possible communicate with the ST2 board with a Fiber Optic connection. Using Modbus RTU protocol, or with IEC 60870-5101 protocol, is possible monitoring all the physical quantities measured (temperature, solar intensity, current, digital inputs
state ecc...). Moreover is possible keep monitored the fuses status on the string box, through the reading of an internal register on memory map (30034).


In the following image there are the "ST2 string controller" with all the wiring. Obviously isn't necessary connect all the specified devices, they are indicated to give a connection general idea.


## 3 HARDWARE CHARACTERISTICS

### 3.1 Hardware Characteristics

ELECTRIC CHARACTERISTICS

| Microprocessor | STM32F303 |
| :---: | :---: |
| Power supply | 24 Vdc |
| Power consumption (W) | < 3 W |
| Maximum number of monitored strings | 8 |
| Maximum common voltage | 1500 V with precision better than $0,5 \%$ |
| Max. current for each string | 25 A |
| Range of measurement | 0 ... 200 A |
| Current reading accuracy | Better than 0,15\% |
| Current reading precision | Typical 0,5 \% |
| Communication | Fiber Optic |
| Digital Inputs | 4 digital inputs 24 Vdc PNP |
| Analog inputs | 1 input PT100, 1 current input ( $0 \ldots 20 \mathrm{~mA}$ ) and 1 voltage input ( $0 \ldots 10 \mathrm{~V}$ ) for solarimeter + one on board temperature sensor to know the temperature inside the string box panel |
| Working temperature's range | From -20 to $+80{ }^{\circ} \mathrm{C}$ |
| Working atmosphere | Without corrosive gas |
| ID Address | Defined by dip-switches |
| Size (naked) | $208,9 \times 108 \mathrm{~mm}$ |
| Size (with support for din rail bar) | $212,9 \times 128 \mathrm{~mm}$ |
| Working humidity | Lower 95 \% without condensation |
| MTBF | > 500000 hours |
| Maximum Operating Altitude | 4000 meters |
| Minimum Current | 1 A (programmable) ${ }^{\text {a }}$ ( from Firmware Version : |
| Minimum Voltage | 100 V (programmable) 1.31 forward |


| $\mathbf{N}^{\circ}$ | Type of resources | Symbol | Terminal Block |
| :---: | :--- | :---: | :---: |
| $\mathbf{1}$ | PT100 input (from -20 to $+120^{\circ} \mathrm{C}$ ) to temperature reading, with <br> precision better than $1,5 \%$ | T1 | CN4 |
| $\mathbf{1}$ | Sensor on board to switch board's temperature reading (precision better <br> than $1,5 \%)$ | T2 | On board |
| $\mathbf{1}$ | Analog input from 0 to 10 V for solarimeter connection | ADC2 | CN5 |
| $\mathbf{1}$ | Analog input from 0 to 20 mA with precision better 1,5\% | ADC1 | CN5 |
| $\mathbf{4}$ | PNP digital inputs 24 Vdc, typically used to arrester connection, <br> switches or other devices | INPO, INP1 <br> INP2, INP3 | CN5 |
|  | Fiber Optic connection : is used to connect many "ST2 string <br> controllers" into a network or to a PC. Is possible select the <br> communication characteristics with a dip-switches on board (node <br> address, baud rate, parity, and communication protocol, that may be <br> Modbus RTU or IEC 60870-5-101). It's divided in 2 connectors (Single <br> Ring) or 4 connectors (Double Ring) | FIBER OPTIC | CN2 |
| $\mathbf{8}$ | This board can manage the current reading of 8 strings until 25 A with <br> typical precision of 0,5 $\%$ and a temperature between -20 and $+80^{\circ} \mathrm{C}$ | Ch01 ... Ch08 | CN6, CN7 |

### 3.2 Power Dissipation

First of all we have to distinguish from "power dissipation" to "power consumption" : power dissipation is the total amount of power dissipated by the SMU, that is splitted in two parts : the part needed to supply the electronics and the part dissipated by the shunts used to measure the string currents. Only the part needed by the electronic (the power consumption) is to be supplied by the external 24 V power supply, this part is fixed and is typically less then $3 \mathrm{~W}(125 \mathrm{~mA})$ for all models of SMU.

The part dissipated by the shunt is variable and depends by the number of shunts and by the current that flows into them. To calculate the dissipation of each channel you can use the following formula, valid for all the SMU model :

## Shunt_Power = current * current * 0.001 (where 0.001 is the ohmic resistance of the shunt) than you have to multiply for the number of channels (shunts)

- Example : ST01625 you have 25 * 25 * 0.001 * $16=10.0 \mathrm{~W}$

Otherwise if you consider a current of 12 A the power dissipated onto the shunt drops to 2.3 W

- Example : ST22422 you have 22 * 22 * 0.001 * $24=11.6 \mathrm{~W}$

Otherwise if you consider a current of 20 A the power dissipated onto the shunt drops to 9.6 W
This part of power is not to be supplied by the 24 V external power supply because it is directly generated by the current from PV strings.

## IMPORTANT

It is possible to power the board directly from PV panels using an insulated DC/DC power supply for PV usage. Of course keep in mind that before the sunrise and after the sunset the voltage from the PV is zero, so the DC/DC converter does not work and the board is not powered.


## ST2 0825NxIDR



CNP

CN3

## Kernel

Sistemi


CN2


### 3.3 Connector: CN1



### 3.4 Connector: CN3

To read the voltages, is necessary connect the negative of the panel's string to one channel and the positive to the positive pin of connector CN3. You'll find the voltage value on 30040 .


### 3.5 Connector: CN4

Is possible connect one PT100 to read the external temperature. The PT100 could be 2 or 3 wires, it will be connected in the following way to terminal block CN4. You can find this temperature on register 30044.


### 3.6 Connector : CN5

To know when a switch (for example the general one) is ON or OFF, there are four digital inputs PNP 24 Vdc on terminal block CN5. Each digital input status is indicated also by a led status on board. You need to use the pins 5, 6, 7, 8, 9 and 10. Inside the memory map the bits from 0 to 3 of register 30001 are the digital input status.


May be necessary connect a probe to a junction box, for example a pyrometer (to know the solar irradiation). To this purpose there are two analog inputs: one which allow to connect a signal which can be from 0 to 20 mA and the other one which allow to connect a signal from 0 to 10 Vdc . The connection is to pin 1, 2, 3 and 4 of terminal block CN5. On register 30042 and 30043 you can find a value between $0 \ldots 1000$ in order to the signal value ( 20 mA or $10 \mathrm{Vdc}=1000 ; 10 \mathrm{~mA}$ or $5 \mathrm{Vdc}=500 ; 0 \mathrm{~mA}$ or 0 $\mathrm{Vdc}=0$ etc...)


### 3.7 Memory Protection

Memory is always protected by writing! In order to enable writing for a limited time ( 10 minutes), you have to write :

Value : 0x5555 Hex
Modbus Address : 40100

### 3.8 Board dimensions (without supporting box)

ST2 0825 DR

3.9 Board dimensions (with support for din rail bar)


### 3.10 Busbar

Examples of busbar connection :


If it is necessary to "extend" the busbar, the following fundamental rules must be followed :

1. The added bar must take the whole bar and be connected to all the bolts (with the correct tightening torque).
2. It must not have curves or irregularities.
3. It must be absolutely flat so as to be perfectly in contact on the whole surface of the bar.

This connection busbar MUST be perfectly planar.
It is therefore necessary to check that these busbars are perfectly planar BEFORE fixing them to the busbar of the board.
It is necessary FIRST to fix the "busbar connection to the Switch Disconnector" to the BOARD, then perform the fastening of this busbar to the Switch Disconnector. In doing so, the perfect contact between the "busbar connection to the S.D." and the busbar on the board is guaranteed.

## ATTENTION

The dimensions of the bar must be calculated according to the value of the MAXIMUM CURRENT (follow the appropriate regulatory tables)
As an alternative to the bar, it is possible to connect more CABLES to the busbar on the board : also in this case the cables must be appropriately sized according to the value of the MAXIMUM CURRENT (follow the appropriate regulatory tables).

### 3.11 Dip-switches



Note : Parity = NO_PARITY ; Bit = 8 ; Stop = 1
Some dip-switches examples :

|  | $\begin{aligned} & \text { Single } / \text { Double Ring } \\ & \text { Protocol }=I E C 101 \\ & \text { Baud Rate }=19200 \\ & \text { Address }=4 \end{aligned}$ |
| :---: | :---: |
|  | Multidrop <br> Protocol = MODBUS <br> Baud Rate $=9600$ <br> Address $=1$ |

### 3.12 Fixing system of the naked board (without supporting box)

To fix the naked board (without case) is necessary use plastic spacers with dual clutch. The plastic spacers must be $4 \times 20 \mathrm{~mm}$ or $4 \times 25 \mathrm{~mm}$ ( 4 mm is the hole diameter on the board). Look the below picture.

### 3.13 Status led



On the board there is a status led which with its blinking show the board status.
There are two possible different blinking ways: blinking each $0,5 \mathrm{sec}$, or blinking faster. If the blinking is 0,5 sec ON and 0,5 sec OFF, it means that the board is ready to communicate with an external device, instead if the blinking is faster than 0,5 sec, it means that the board is in test mode with all the dip-switch OFF. In this way the board isn't ready to communicate with an external device.

### 3.14 Informations about wires and connectors



| CHANNEL CONNECTOR |  |
| :---: | :---: |
| Nominal Cross Section | $6 \mathrm{~mm}^{2}$ |
| Stripping length | 8 mm |
| Conductor cross section <br> AWG/kcmil | 24 to 10 |
| Screw | M 3 |
| Min / Max Torque | $0.50 \mathrm{Nm} / 0.60 \mathrm{Nm}$ <br> $[4.4 \mathrm{Lbf}-\mathrm{In} / 5.3 \mathrm{Lbf}-\mathrm{In}]$ |
| Operating temperature | $-40^{\circ} \mathrm{C} \div$ (depends on the derating curve) |



| VOLTAGE CONNECTOR |  |
| :---: | :---: |
| Wire Section | $2.5 \mathrm{~mm}^{2} / 0.20-2.5 \mathrm{~mm}^{2}$ |
| Stripping length | 7 mm |
| Conductor cross section <br> AWG/kcmil | 24 to 12 |
| Screw | M 3 |
| Min / Max Torque | $0.50 \mathrm{Nm} / 0.60 \mathrm{Nm}$ <br> $[4.4 \mathrm{Lbf}-\mathrm{In} / 5.3 \mathrm{Lbf}-\mathrm{In}]$ |
| Operating temperature | $-40^{\circ} \mathrm{C} \div$ (depends on the derating curve) |



| OTHER CONNECTORS (Power Supply, Serial ...) |  |
| :---: | :---: |
| Wire Section | $2.5 \mathrm{~mm}^{2} / 0.34-2.5 \mathrm{~mm}^{2}$ |
| Stripping length | $7-8 \mathrm{~mm}$ |
| Solid Wire (AWG) | $12-24 / 14-22$ |
| Screw | M 3 |
| Max Torque | $0.56 \mathrm{Nm}[5.0 \mathrm{Lbf}-\mathrm{In}]$ |
| Operating temperature | $-40^{\circ} \mathrm{C} \div+115^{\circ} \mathrm{C}$ |



## Copper Bar

Min / Max Torque
2.2 Nm / 2.5 Nm [19.5 Lbf-In / 22.1 Lbf-In]

### 3.15 Connection Types

Is possible connect ST2 with 3 different configurations as below! The 3 types are the following :

1. "Single Ring or Single Connection"
2. "Double Ring or Double Connection" [Redundant]
3. "Multidrop Connection" [Redundant]

The final result is as schematized here below :

## SINGLE RING / SINGLE CONNECTION



Protocol = MODBUS or IEC 60870-5-101

DOUBLE RING / DOUBLE CONNECTION [REDUNDANT]


Protocol $=$ MODBUS or IEC 60870-5-101

## MULTIDROP CONNECTION [REDUNDANT]



Protocol = MODBUS or IEC 60870-5-101

### 3.16 Scada - PLC

Typically the PLC communicates via Modbus TCP / IP protocol with the SCADA in the control room with Ethernet or fiber optic or wireless connection.

ETHERNET
CONTROL ROOM


FIBER OPTIC


WIRELESS
CONTROL ROOM



## 4 MEMORY MAP

The ST2 has the following memory map, it's made of 16 bits locations ( 1 word) called "REGISTERS". Because each REGISTER is composed by 16 bits, its maximum value will be 65535 .

| MODBUS Register | TYPE | DESCRIPTION |
| :---: | :---: | :---: |
| 30001 | RO | Inputs |
| 30002 | RO | Inst Curr Str_01 (mA [0...25000]) |
| 30003 | RO | Inst Curr Str_02 (mA [0...25000]) |
| 30004 | RO | Inst Curr Str_03 (mA [0...25000]) |
| 30005 | RO | Inst Curr Str_04 (mA [0...25000]) |
| 30006 | RO | Inst Curr Str_05 (mA [0...25000]) |
| 30007 | RO | Inst Curr Str_06 (mA [0...25000]) |
| 30008 | RO | Inst Curr Str_07 (mA [0...25000]) |
| 30009 | RO | Inst Curr Str_08 (mA [0...25000]) |
| $\ldots$ |  |  |
| 30034 | RO | Fuse status (Ch01...Ch08) |
| $\ldots$ |  |  |
| 30040 | RO | Inst V_1 (V [0... 1500]) |
| 30041 | RO | Not used |
| 30042 | RO | Inst Aux 1 (0 ... 10 V ) [0...1000] |
| 30043 | RO | Inst Aux 2 (0 ... 20 mA ) [0...1000] |
| 30044 | RO | Inst T_1 ( ${ }^{\circ} \mathrm{C}$ [-20... +120$]$ ) - PT100 |
| 30045 | RO | Inst T_2 ( $\left.{ }^{\circ} \mathrm{C}[-22,0 \ldots+83,0]\right)$ - on board |
| $\cdots$ |  |  |
| 30047 | RO | Sum of all currents (A/10) |
| 30048 | RO | Power (W) - LSW |
| 30049 | RO | Power (W) - MSW |


| 30052 | RO | RMS Curr Str_01 (average value on last 6 seconds) |
| :---: | :---: | :---: |
| 30053 | RO | RMS Curr Str_02 (average value on last 6 seconds) |
| 30054 | RO | RMS Curr Str_03 (average value on last 6 seconds) |
| 30055 | RO | RMS Curr Str_04 (average value on last 6 seconds) |
| 30056 | RO | RMS Curr Str_05 (average value on last 6 seconds) |
| 30057 | RO | RMS Curr Str_06 (average value on last 6 seconds) |
| 30058 | RO | RMS Curr Str_07 (average value on last 6 seconds) |
| 30059 | RO | RMS Curr Str_08 (average value on last 6 seconds) |
|  |  |  |
| 30084 | RO | RMS V_1 (V [0...1500]) (average value on last 6 seconds) |
| 30085 | RO | Not used |
| 30086 | RO | RMS Aux 1 (0 ... 10 V ) [0...1000] (average value on last 6 seconds) |
| 30087 | RO | RMS Aux 2 (0 ... 20 mA ) [0...1000] (average value on last 6 seconds) |
| 30088 | RO | RMS Inst T_1 ( ${ }^{\circ} \mathrm{C}$ [-20... +120$]$ ) (average value on last 6 seconds) |
| 30089 | RO | RMS Inst T_2 ( ${ }^{\circ} \mathrm{C}[-22,0 \ldots+83,0]$ ) (average value on last 6 seconds) |

...

| 30091 | RO | RMS Sum of all currents (A/10) (average value on last 6 seconds) |  |
| :---: | :---: | :---: | :---: |
| 30092 | RO | RMS Power (W) - LSW (av |  |
| 30093 | RO | RMS Power (W) - MSW (av |  |
| ... |  |  |  |
| 30201 | RO | Firmware Version | READ ON |
| 30202 | RO | SMU Model | READ ON |
| 30203 | RO | Channels Number | READ ON |
| 30204 | RO | Shunt Type | READ ON |
| 30205 | RO | End Scale | READ ON |
| 30206 | RO | Reserved for Future Use | RFU |
| 30207 | RO | Reserved for Future Use | RFU |
| 30208 | RO | Reserved for Future Use | RFU |
| 30209 | RO | Reserved for Future Use | RFU |
| 30210 | RO | Reserved for Future Use | RFU |
| 30211 | RO | Unique ID code [0] | READ ON |
| 30212 | RO | Unique ID code [1] | READ ON |
| 30213 | RO | Unique ID code [2] | READ ON |
| 30214 | RO | Unique ID code [3] | READ ON |
| 30215 | RO | Unique ID code [4] | READ ON |
| 30216 | RO | Unique ID code [5] | READ ON |
| $\stackrel{\square}{ }$ |  |  |  |
| 40001 | RW | Set up PARITY mode : 1 = None ; 2 = Even ; 3 = Odd |  |
| 40002 | RW | Offset Curr Str_01 |  |
| 40003 | RW | Offset Curr Str_02 |  |
| 40004 | RW | Offset Curr Str_03 |  |
| 40005 | RW | Offset Curr Str_04 |  |
| 40006 | RW | Offset Curr Str_05 |  |
| 40007 | RW | Offset Curr Str_06 |  |
| 40008 | RW | Offset Curr Str_07 |  |
| 40009 | RW | Offset Curr Str_08 |  |
| .. |  |  |  |
| 40034 | RW | Answer Delay (msec) |  |
| 40035 | RW | Time Com Active (1/10 sec.) |  |
| 40036 | RW | Parity ( 1 = None, 2 = Even, 3 = Odd $)=40001$ |  |
| 40037 | RW | Fuse Threshold |  |
| $\cdots$ |  |  |  |
| 40040 | RW | Offset V_1 |  |
| 40041 | RW | Not used |  |
| 40042 | RW | Offset Aux_1 |  |
| 40043 | RW | Offset Aux_2 |  |
| 40044 | RW | Offset T_1 |  |
| 40045 | RW | Offset T_2 |  |
| $\ldots$ |  |  |  |
| 40047 | RW | Minimum Current (Default $=1 \mathrm{~A}$ ) |  |
| 40048 | RW | Minimum Voltage (Default $=100 \mathrm{~V}$ ) |  |


| 40052 | RW | Gain Curr Str_1 |
| :---: | :---: | :---: |
| 40053 | RW | Gain Curr Str_2 |
| 40054 | RW | Gain Curr Str_3 |
| 40055 | RW | Gain Curr Str_4 |
| 40056 | RW | Gain Curr Str_5 |
| 40057 | RW | Gain Curr Str_6 |
| 40058 | RW | Gain Curr Str_7 |
| 40059 | RW | Gain Curr Str_8 |
|  |  |  |
| 40090 | RW | Gain V_1 |
| 40091 | RW | Not used |
| 40092 | RW | Gain Aux_1 |
| 40093 | RW | Gain Aux_2 |
| 40094 | RW | Gain T_1 |
| 40095 | RW | Gain T_2 |
|  |  |  |
| 40101 | RW | User Memory 01 |
| 40102 | RW | User Memory 02 |
| 40103 | RW | User Memory 03 |
| 40104 | RW | User Memory 04 |
| 40105 | RW | User Memory 05 |
| 40106 | RW | User Memory 06 |
| 40107 | RW | User Memory 07 |
| 40108 | RW | User Memory 08 |


| 40201 | RO | Shunt Type (=30204) | READ ONLY |
| :--- | :--- | :--- | :--- |
| 40202 | RO | SMU Model $(=30202)$ | READ ONLY |
| 40203 | RO | Firmware Version $(=30201)$ | READ ONLY |
| 40204 | RO | Channels Number $(=30203)$ | READ ONLY |
| 40205 | RO | End Scale $(=30205)$ | READ ONLY |
| 40206 | RO | Reserved for Future Use | RFU |
| 40207 | RO | Reserved for Future Use | RFU |
| 40208 | RO | Reserved for Future Use | RFU |
| 40209 | RO | Reserved for Future Use | RFU |
| 40210 | RO | Unique ID code [0] $(=30211)$ | READ ONLY |
| 40211 | RO | Unique ID code [1] $(=30212)$ | READ ONLY |
| 40212 | RO | Unique ID code [2] $(=30213)$ | READ ONLY |
| 40213 | RO | Unique ID code [3] $(=30214)$ | READ ONLY |
| 40214 | RO | Unique ID code [4] $(=30215)$ | READ ONLY |
| 40215 | RO | Unique ID code [5] $(=30216)$ | READ ONLY |

## NOTES

Each "Offset Register" has 0 as default value. Each "Gain Register" has 1000 as default value. The value 1000 means x1, in this way, for example, is possible write 500 and make the value $\mathbf{x 0 , 5}$.

### 4.1 Memory Map Description

30001 : the first four bits of these register are the mirror status of the four digital inputs on the board (INPO $\qquad$ So if $30001=0000000000001111[\mathrm{bin}]=15[\mathrm{dec}]$, it means that all the four digital inputs are ON .
$30002 \ldots 30009$ : these registers contains the current value of the current reading on each channel. It is in mA
30034 : the first eight bits of 30034 show if each channel current reading is under 200 mA or not. This threshold represent the fuse status.

30040 ... 30049 : these registers show the value of some readings as the temperatures (T1 and T2), voltage reading (on connector CN3) etc...

30052 ... 30059 : these registers contains the average value on last 6 seconds of the current reading. Obviously these values are more stable than the instantaneous values show in registers 30002 ... 30009

40001 : through this register is possible set the communication parity. The default value is zero, so "no parity"
40002 ... 40009 : these are the offset registers. These registers (whose default value is 0 ) allow to add a constant value to the current reading. This allow to adjust a possible reading error. For example if 30002 show 2300 (it means that channel CH1 read $2,3 \mathrm{~A}$ ), writing $40002=200$ the new value of the reading will be $30002=2500$ (it means that channel CH 1 read $2,5 \mathrm{~A}$ ).

40052 ... 40059 : these are the gain registers. These registers (whose default value is 1000 ) allow to multiply a constant value to the current reading. This allow to adjust a possible reading error. For example if 30002 show 2300 (it means that channel CH 1 read $2,3 \mathrm{~A}$ ), writing $40052=1500$ the new value of the reading will be $30002=3450$ (it means that channel CH1 read $3,45 \mathrm{~A}, 2300 \times 1,5=3450$ ).

40101 ... 40108 : these are 8 registers available to the user. They can contain data useful to the customer, for example a different progressive number for each board.

40201 : Shunt Type (= 30204) - READ ONLY
40202 : SMU Model (= 30202) - READ ONLY
40203 : Firmware Version (= 30201) - READ ONLY
40204 : Channels Number (= 30203) - READ ONLY
40205 : End Scale (= 30205) - READ ONLY
40206 ... 40209 : Not Used - Reserved for Future Use (RFU)
40210 ... 40215 : Unique device ID register ( 96 bits) (organized in six 16 bit words) that is unique for any board.
(= 30211 ... 30216) - READ ONLY
The unique device identifier is ideally suited :

- for use as serial numbers (for example string serial numbers or other end applications).
- for use as part of the security keys in order to increase the security.

The 96 -bit unique device identifier provides a reference number which is unique for any device and in any context. These bits cannot be altered by the user. The code is composed of the following parts :

```
UID [31:00] : X and Y coordinates on the wafer expressed in BCD format
UID [39:32] : WAF NUM [07:00] > Wafer number (8-bit unsigned number)
UID [63:40] : LOT_NUM [23:00] > Lot number (ASCII encoded)
UID [95:64] : LOT_NUM [55:24] > Lot number (ASCII encoded)
```


### 4.2 Reading speed

The analogic values of the currents, the voltage and the temperature are read simultaneously 10 times per second ( 100 msec scan time), then are inserted in it's own FIFO (a FIFO for each analogic value), 16 values deep. The value read from the board is the mobile mean of the FIFO, so it is the mean of the last 16 read values ( 1.6 sec ), updated every 100 msec . This is done to make the analogic readout more stable and it is a good compromise between speed and readout stability.
The instant values of the analogic are temporary stored into a hidden memory area, not accessible to the COM port. The update time depend on the speed polling time of the SCADA and the communication baud rate.

The total time requested to read the board via com port is splitted into three times: the query time, the latency time and the response time.

The query time is the time needed by the SCADA to send the MODBUS query packet and depends on the baud rate (about 4 msec at 19200 baud rate).

The latency time is the time need by the board to process the query and prepare the answer, it is between 1 and 2 msec and it is independent on the baud rate.

The response time is the time needed byte the board to send the MODBUS answer packet, it's depend on the baud rate and on the number of registers read at a time, for a single register read at 19200 baud it is about 4 msec .
So at 19200 baud rate the total time needed to read a single register is about $10 \mathrm{msec} .$, you have to add 1 msec every other register read, for example to read 16 registers with a single query will take $10 \mathrm{msec}+15 * 1 \mathrm{msec}=25 \mathrm{msec}$.


## 5 OPTICAL FIBER

### 5.1 OFC Network features

The characteristics of the fiber optic network achievable with the KERNEL SMUs with OFC interface on board are the following :

| Max number of SMU that it's possible to connect |
| :---: | :---: |
| in one network |$\quad 256$

## IMPORTANT

The optical signal is regenerated internally to each SMU so that, each SMU retransmits the optical signal at the maximum power.
So the first SMU, connected to the FB2485H converter, receives the same level of optical signal that receives the last SMU of the network.

## IMPORTANT

### 5.2 Optical Fiber Cable

The optical fiber cable which is necessary to the connection, must be a compatible cable :

|  | Compatible with : |
| :---: | :---: |
| $50 / 125 \mu \mathrm{~m}$ |  |
| Optical Fiber | $62.5 / 125 \mu \mathrm{~m}$ |
|  | $100 / 140 \mu \mathrm{~m}$ |
|  | $200 \mu \mathrm{~m}$ Plastic-Clad Silica (PCS) Fiber |
| In Compliance with standard | OM1, OM2, OM3, OM4 |



### 5.3 Optical Fiber Connector

Here are indicated the characteristics of the optical fiber connector on the board :


## 6 ORDER CODES

Here below the order codes:

| CODE | DESCRIPTION |
| :---: | :---: |
| ST2 0825NCISR | Device with support for din rail bar + <br> 2 Fiber Optic Connectors |
| ST2 0825NKISR | Device without support for din rail bar + <br> 2 Fiber Optic Connectors |
| ST2 0825NC\DR | Device with support for din rail bar + <br> 4 Fiber Optic Connectors |
| ST2 0825NK\DR | Device without support for din rail bar + <br> 4 Fiber Optic Connectors |

## 7 CONTACTS

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